



BAT54 Series

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

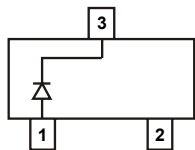
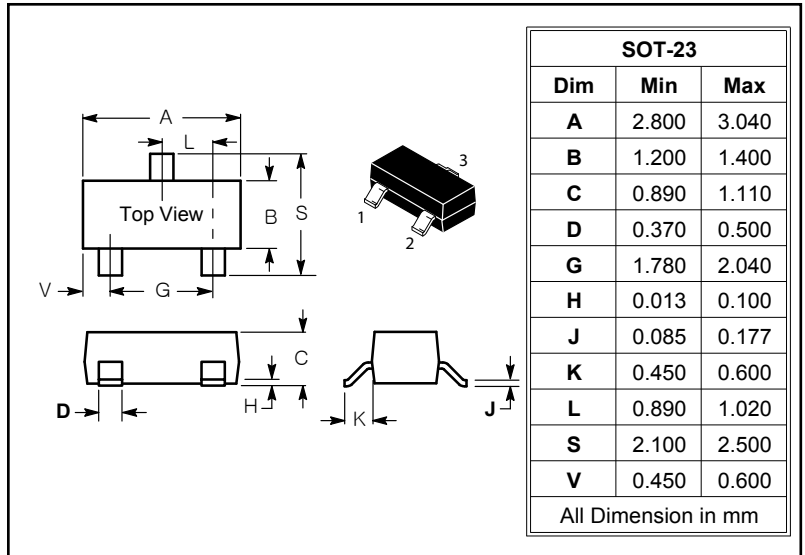
Reverse Voltage - 30 Volts Forward Current - 200 mAmpere

FEATURES

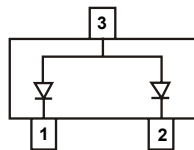
- Low Turn-on Voltage
- Fast Switching
- Ultra-small surface mount package.
- PN Junction Guard Ring for Transient and ESD Protection

MECHANICAL DATA

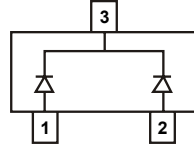
- Case: SOT-23, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagrams Below
- Weight: 0.008 grams (approx.)



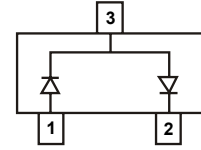
BAT54 Marking : JV3, L4



BAT54A Marking : B6, L42



BAT54C Marking : 5C, L43



BAT54S Marking : LD3, L44

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	30	Volts
Forward Power Dissipation @ T _A = 25°C Derate above 25°C	P _F	225 1.8	mW mW/°C
Forward Current (DC)	I _F	200 Max	mA
Operating and Junction Temperature	T _J	-55 to +125	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

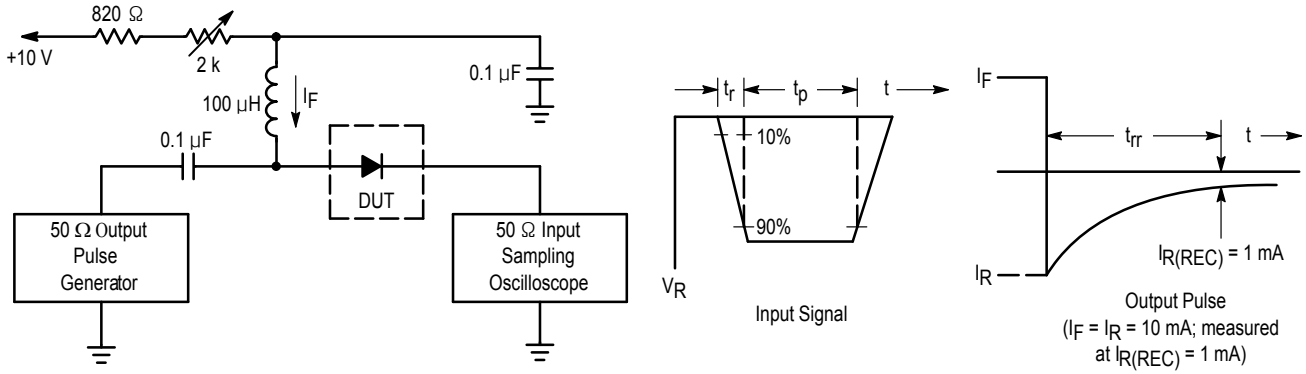
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Typ	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V _{(BR)R}	30	—	—	Volts
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	C _T	—	7.6	10	pF
Reverse Leakage (V _R = 25 V)	I _R	—	0.5	2.0	μAdc
Forward Voltage (I _F = 0.1 mAdc)	V _F	—	0.22	0.24	Vdc
Forward Voltage (I _F = 30 mAdc)	V _F	—	0.41	0.5	Vdc
Forward Voltage (I _F = 100 mAdc)	V _F	—	0.52	1.0	Vdc
Reverse Recovery Time (I _F = I _R = 10 mAdc, I _{R(REC)} = 1.0 mAdc) Figure 1	t _{rr}	—	—	5.0	ns
Forward Voltage (I _F = 1.0 mAdc)	V _F	—	0.29	0.32	Vdc
Forward Voltage (I _F = 10 mAdc)	V _F	—	0.35	0.40	Vdc
Forward Current (DC)	I _F	—	—	200	mAdc
Repetitive Peak Forward Current	I _{FRM}	—	—	300	mAdc
Non-Repetitive Peak Forward Current (t < 1.0 s)	I _{FSM}	—	—	600	mAdc



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RATINGS AND CHARACTERISTIC CURVES



- Notes: 1. A 2.0 kΩ variable resistor adjusted for a Forward Current (I_F) of 10 mA.
 2. Input pulse is adjusted so $I_{R(\text{peak})}$ is equal to 10 mA.
 3. $t_p \gg t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit

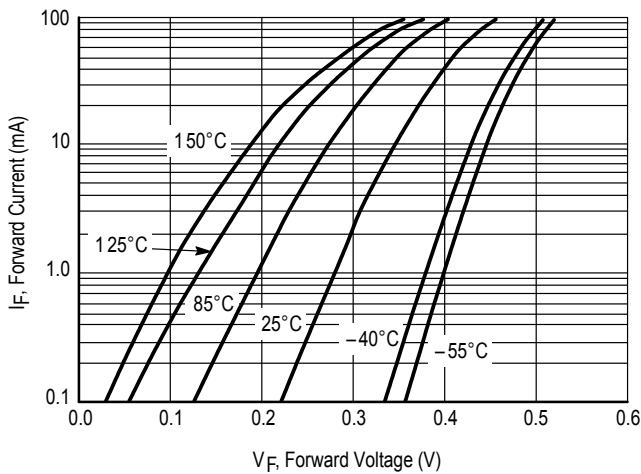


Figure 2. Forward Voltage

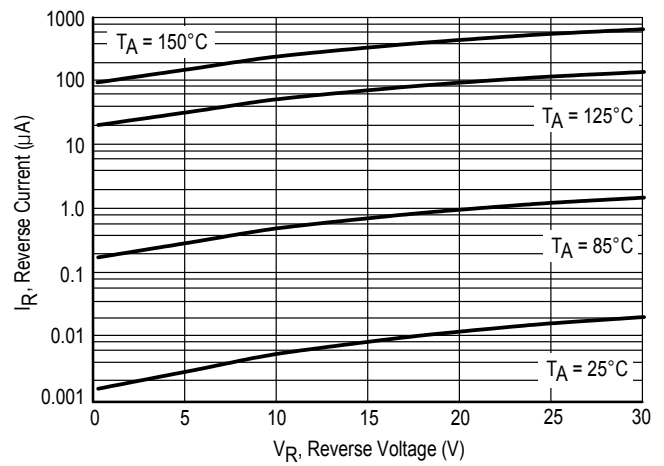


Figure 3. Leakage Current

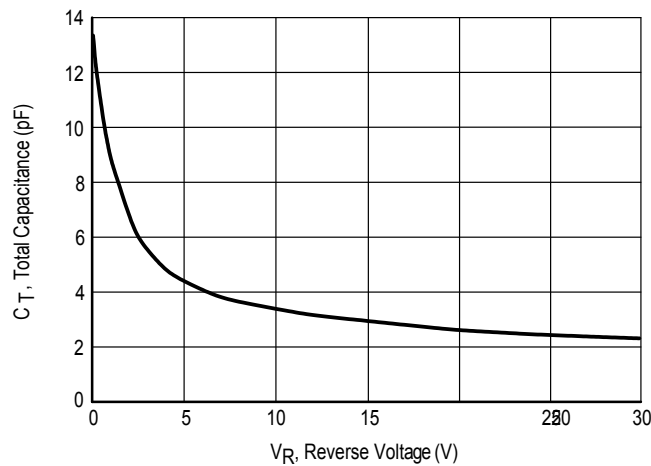


Figure 4. Total Capacitance