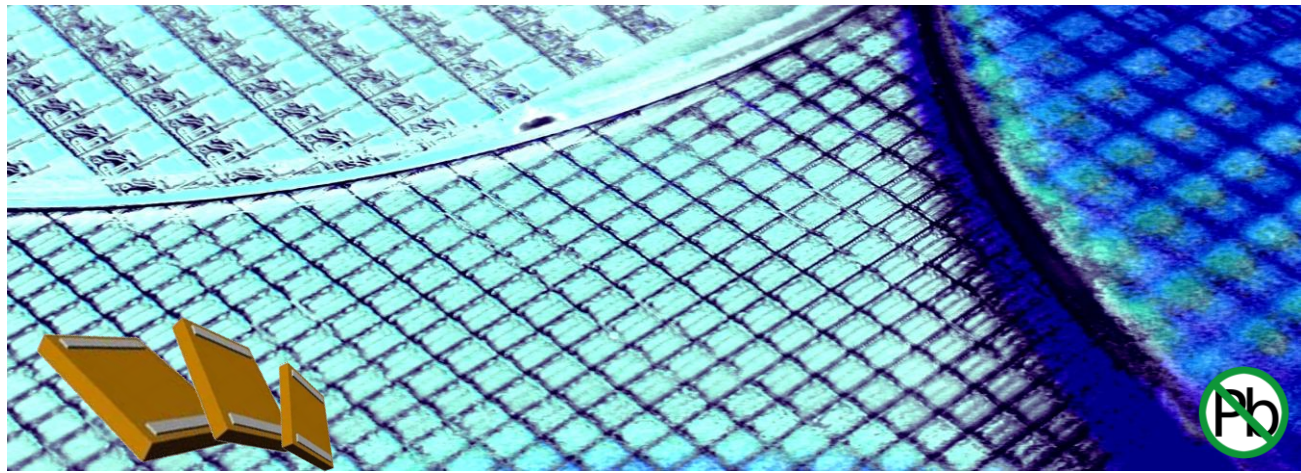


HTSC423.xxx - 0201 High Temperature Silicon Capacitor

Rev 3.1



Key features

- **High stability up to 200°C:**
 - ◆ Temperature $<\pm 1\%$ (-55 °C to +200 °C)
 - ◆ Voltage $<0.1\%$ /V
 - ◆ Negligible capacitance loss through aging
- **Unique high capacitance in EIA/0201 package size, up to 10nF**
- **High reliability (FIT <0.017 parts / billion hours)**
- **Low leakage current down to 100 pA**
- **Low ESL and Low ESR**
- **Suitable for lead free reflow-soldering** *Please refer to our assembly Application Note for further recommendations

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding applications can be solved.

High Temperature Silicon Capacitors are dedicated to applications where **reliability** up to **200°C** is the main parameter.

This technology features a capacitor integration capability (up to $250\text{nF}/\text{mm}^2$) which offers capacitance value similar to X7R dielectric, but with better electrical performances than COG/NPO dielectrics, up to **200°C**.

HTSC provides the highest capacitor **stability** over the full -55°C/+200°C temperature range in the market with a **Temperature Coefficient lower than $\pm 1\%$** .

Key applications

- **All applications up to 200°C, such as military, aerospace and automotive industries**
- **High reliability applications**
- **Replacement of X7R and C0G dielectrics**
- **Decoupling / Filtering / Charge pump (i.e.: motor management, temperature sensors)**
- **Downsizing**

The IPDiA technology offers industry leading performances relative to **failure rate** with a FIT <0.017 .

This technology also offers **high reliability**, up to 10 times better than alternative capacitor technologies, such as Tantalum or MLCC, and eliminates cracking phenomena.

This Silicon based technology is RoHS compliant and compatible with lead free reflow soldering process.

Electrical specification

		Capacitance value					
		10	15	22	33	47	68
Unit	1 pF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales
	10 pF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales
	0.1 nF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales
	1 nF	10nF: 935.132.423.510 935.132.723.510					

(*) Thinner thickness (as low as 100 μm thick) available, see Low Profile Silicon Capacitor product: LPSC
 (**) Extended temperature range (up to +250 °C) available, see Xtreme Temperature Silicon Capacitor product: XTSC
 (***) Other values on request.

Parameters	Value
Capacitance range	10 nF ^(***)
Capacitance tolerances	$\pm 15\%$ ^(***)
Operating temperature range	-55 °C to 200 °C ^(**)
Storage temperatures	-70 °C to 215 °C
Temperature coefficient	$\leq \pm 1\%$, from -55 °C to +200 °C
Breakdown voltage (BV)	11 VDC 30VDC
Capacitance variation versus RVDC	0.1 % /V (from 0 V to RVDC)
Equivalent Serial Inductor (ESL)	Max 100 pH
Equivalent Serial Resistor (ESR)	Max 400m Ω ^(***)
Insulation resistance	50G Ω min @ 3V,25°C 20G Ω min @ 3V,200°C
Ageing	Negligible, < 0.001 % / 1000h
Reliability	FIT<0.017 parts / billion hours,
Capacitor height	Max 400 μm ^(*)

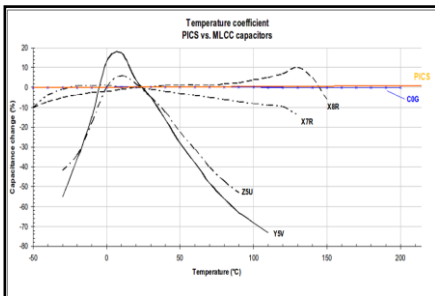


Fig.1 Capacitance change versus temperature variation compared with alternative dielectrics

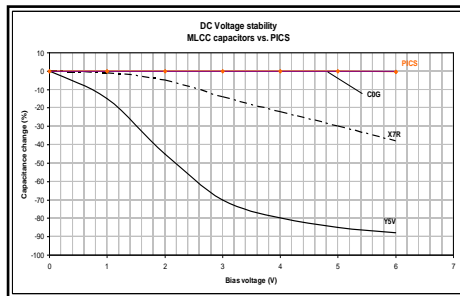


Fig.2 Capacitance change versus voltage variation compared with alternative dielectrics

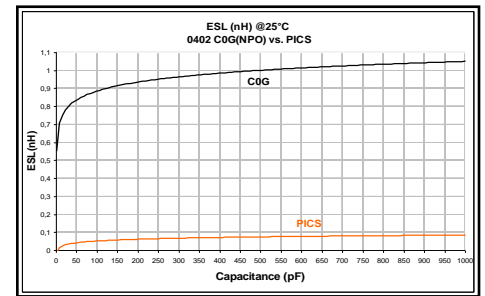


Fig.3 ESL versus capacitance value compared with alternative dielectrics

Part Number

935.132. **B.2** **S.** **U** **XX** → **Value**

i.e.: 10 nF/0201 case (HTSC type)
→ 935.132.423.510

Breakdown Voltage
4 = 11V
7 = 30V

Size
3 = 0201

Unit
0 = 10 f 5 = 1 n
1 = 0.1 p 6 = 10 n
2 = 1 p 7 = 0.1 μ
3 = 10 p 8 = 1 μ
4 = 0.1 n 9 = 10 μ

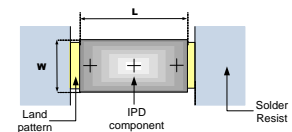
Termination and Outline

Termination
Lead-free nickel/solder coating compatible with automatic soldering technologies: reflow and manual.

Typical dimensions, all dimensions in mm.

Package outline

Typ.		0201
Comp. size	L	0.8 \pm 0.03
	W	0.60 \pm 0.03



(0201 PCB footprint)

Packaging

Tape and reel, tray, waffle pack or wafer delivery.

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