Features

- **Temperature and Supply Voltage Compensated Flashing Frequency**
- **Frequency Doubling Indicates Lamp Outage**
- **Two Relay Driver Outputs with High Current-carrying Capacity and Low Saturation Voltage**
- **Minimum Lamp Load for Flasher Operation: $\geq$ 1W**
- **Very Low Susceptibility to EMI**
- **Protection According to ISO/TR7637/1 Level 4**
- **Extremely Low Current Consumption < 10 µA (with Switches Open)**
- **Reverse Polarity Protection**
- **Three Control Inputs: Left, Right and Hazard Warning**

1. Description

The integrated circuit U2044B is used in relay-controlled automotive flashers. With two output stages, each side of the vehicle is controlled separately. A left and a right direction indicator input with only a small control current makes switch contacts for small loads possible.

The separate hazard warning input simplifies the construction of the hazard switch. Lamp outage is indicated by frequency doubling during direction mode. Thanks to extreme low current consumption the U2044B can be directly connected to the battery.
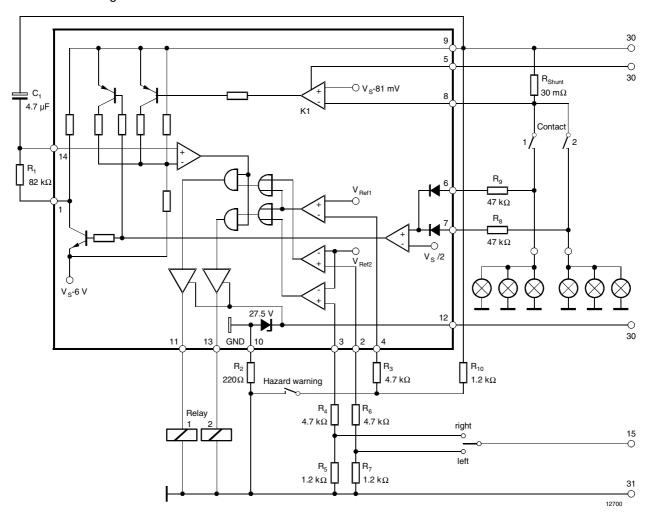
Dual Output Flasher

U2044B

Figure 1-1. Block Diagram
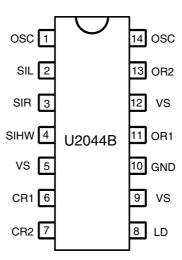
2. Pin Configuration

Figure 2-1. Pinning DIP14/SO14

```
        OSC  1          14  OSC
        SIL  2          13  OR2
        SIR  3          12  VS
       SIHW  4  U2044B  11  OR1
         VS  5          10  GND
        CR1  6           9  VS
        CR2  7           8  LD
```

Table 2-1. Pin Description

Pin	Symbol	Function
1	OSC	Oscillator
2	SIL	Start input left
3	SIR	Start input right
4	SIHW	Start input hazard warning
5	VS	V_S
6	CR1	Control input relay 1
7	CR2	Control input relay 2
8	LD	Lamp failure detection
9	VS	V_S
10	GND	IC ground
11	OR1	Output relay 1
12	VS	V_S
13	OR2	Output relay 2
14	OSC	Oscillator

3. Functional Description

3.1 Oscillator (Pin 1 and 14)

Flashing frequency, f_1, is determined by the R_1C_1 components as follows (see Figure 1-1 on page 2):

$$f_1 \approx \frac{1}{R_1 \times C_1 \times 1.5} Hz$$

where
$$C_1 \leq 47 \ \mu F$$
$$R_1 = 6.8 \ k\Omega \ \text{to} \ 180 \ k\Omega$$

In the case of a lamp outage, the oscillator frequency is switched to the lamp outage frequency f_2 with $f_2 \approx 2.2 \times f_1$.

Duty cycle in normal flashing mode: 50%
Duty cycle in lamp outage mode: 40% (bright phase)

3.2 Start Input Right and Left (Pin 2 and 3)

Flashing is disabled as long as the input comparator is tied to GND (pull-down resistor R_7 or R_5). The high-side flasher switch left or right changes the comparator status and enables the output stage at pin 11 or pin 13. R_6 and R_4 are protection resistors for the input stage.

With an open flasher switch the current consumption is only I < 10 µA. The IC is kept in stand-by mode until there is a voltage drop of V ≈ 6.9V at the pull-down resistor.

Direction mode can only be activated when the ignition switch is in the ON-position as shown in Figure 1-1 on page 2.

3.3 Start Input Hazard Warning (Pin 4)

In contrast to the direction switches, the hazard input is a low-side type. The pull-up resistor R_{10} provides the off-state. R_3 is a protection resistor for the input stage. Hazard warning can be activated independent of the ignition switch position.

3.4 Supply Voltage Sense (Pin 5)

This pin supplies the lamp outage comparator at pin 8 and is externally connected to the battery (Kl 30).

3.5 Control Input Relay 1 and 2 (Pin 6 and 7)

The feedback detects the bright phase and the dark phase and enables the oscillator.

3.6 Lamp Outage Detection (Pin 8)

The lamp current is monitored via an external shunt resistor, R_{Shunt} and an internal comparator, K1, with its reference voltage of typically 81 mV ($V_S = 12V$). The outage of one lamp out of two lamps is detected according to the following calculation:

Nominal current of 1 lamp: 21W/($V_S = 12V$): $I_{lamp} = 1.75A$
Nominal current of 2 lamps: $2 \times$ 21W/($V_S = 12V$): $I_{lamp} = 3.5A$

We recommend setting the detection threshold in the middle of the current range: $I_{outage} \approx 2.7A$

Thus the shunt resistor is calculated as:

$R_{Shunt} = V_T$ (K1)/I_{outage}
$R_{Shunt} = 81$ mV/2.7 A = 30 mΩ

Comparator K1's reference voltage is matched to the characteristics of filament lamps (see section "Control Signal Threshold").

The combination of the shunt resistor and the resistance of the wire harness prevents pin 8 from a too high voltage in the case of shorted lamps.

3.7 Supply Voltage (Pin 9)

This pin supplies the oscillator, the comparators and the logic parts of the IC.

3.8 GND (Pin 10)

The integrated circuit is protected against transients according to ISO-TR 7637-3 level 3 via resistor R_2 to ground (–31). An integrated protection circuit together with external resistors R_2, R_3, R_4, R_6, R_8 and R_9 limits the current pulses in the IC. The IC is also protected against reversed battery.

3.9 Control Output Relay 1 and 2 (Pin 11 and 13)

The relay control outputs are high-side drivers with a low saturation voltage and capable of driving a typical automotive relay with a coil resistance of 60Ω.

3.10 Supply Voltage Power (Pin 12)

This pin supplies the relay drivers connected directly to the battery (Kl 30). It is internally clamped by a 27-V Z-diode.

4. Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Reference point pin 1

Parameters	Symbol	Value	Unit
Supply voltage, 1 min, pins 5, 9 and 12	V_S	24	V
Junction temperature	T_j	150	°C
Ambient temperature range	T_{amb}	−40 to +100	°C
Storage temperature range	T_{stg}	−55 to +150	°C

5. Thermal Resistance

Parameters	Symbol	Value	Unit
Junction ambient, DIP14	R_{thJA}	90	K/W
Junction ambient, SO14	R_{thJA}	120	K/W

6. Electrical Characteristics

Typical values under normal operation in application circuit Figure 1-1 on page 2, V_S (+30) = 12V.
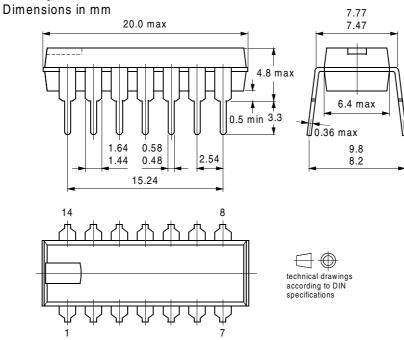Reference point ground (−31), T_{amb} = 25°C, unless otherwise specified

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Supply voltage range	Pins 5, 9, 12	V_S	8		18	V
Supply current, switches open	Pins 5, 9, 12	I_S			10	µA
Output current for relay driver	Pins 11, 13	I_O			300	mA
Saturation voltage	$R_L = 82\Omega$ $V_S = 8V$ $V_S = 12V$	V_O			1.0 1.2	V V
Relay output reverse current	Pin 11, 13	I_O			0.1	mA
Relay coil resistance		R_L	60			Ω
Start delay	First bright phase	t_{on}			10	ms
Control signal threshold	$V_S = 9V$ $V_S = 13.5V$ $V_S = 16V$	V_S V_S V_S		70.6 85.0 93.0		mV mV mV
Tolerance of control signal threshold	$V_S = 9V$ to 16V, pin 8, $T_{amb} = -140°C$ to 100°C		−6		+6	%
Temperature coefficient of control signal threshold	$V_S = 13.5V$, pin 8	T_K		10		µV/K
Clamping voltage	$T_{amb} = -40°C$ to 100°C	V_{12}	25.0	27.5	30.0	V
Relay output overvoltage detection (relay disabled)	$T_{amb} = -40°C$ to 100°C	V_{12}	18	20	22	V

7. Tolerances

Typical values under normal operation in application circuit Figure 1-1 on page 2, V_S (+30) = 12V.
Reference point ground (–31), T_{amb} = 25°C, unless otherwise specified

Parameters	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Frequency determining resistor		R_1	6.8		510	kΩ
Frequency determining capacitor		C_1			47	µF
Frequency tolerance	Normal flashing, basic frequency f_1 not including the tolerance of the external components R_1 and C_1	Δf_1	–5		+5	%
Bright period	Basic frequency f_1	Δf_1	47		53	%
	Control frequency f_2	Δf_2	37		45	%
Frequency increase	Lamp failure	f_2	$2.15 \times f_1$		$2.3 \times f_1$	Hz
Lamp load		P_L	1			W

8. Ordering Information

Extended Type Number	Package	Remarks
U2044B-MY	DIP14	Tubed, Pb-free
U2044B-MFPY	SO14	Tubed, Pb-free
U2044B-MFPG3Y	SO14	Taped and reeled, Pb-free

9. Package Information

Package DIP14
Dimensions in mm

technical drawings
according to DIN
specifications

Package SO14
Dimensions in mm

8.75

1.4

0.4

0.25
0.10

1.27

7.62

14

8

5.2
4.8

3.7

0.2

3.8

6.15
5.85

1

7

technical drawings
according to DIN
specifications

10. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

Revision No.	History
4721B-AUTO-09/05	• Put datasheet in a new template • Pb-free logo on page 1 added • New heading rows in Table "Absolute Maximum Rating" on page 6 added • Ordering Information on page 7 changed

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